

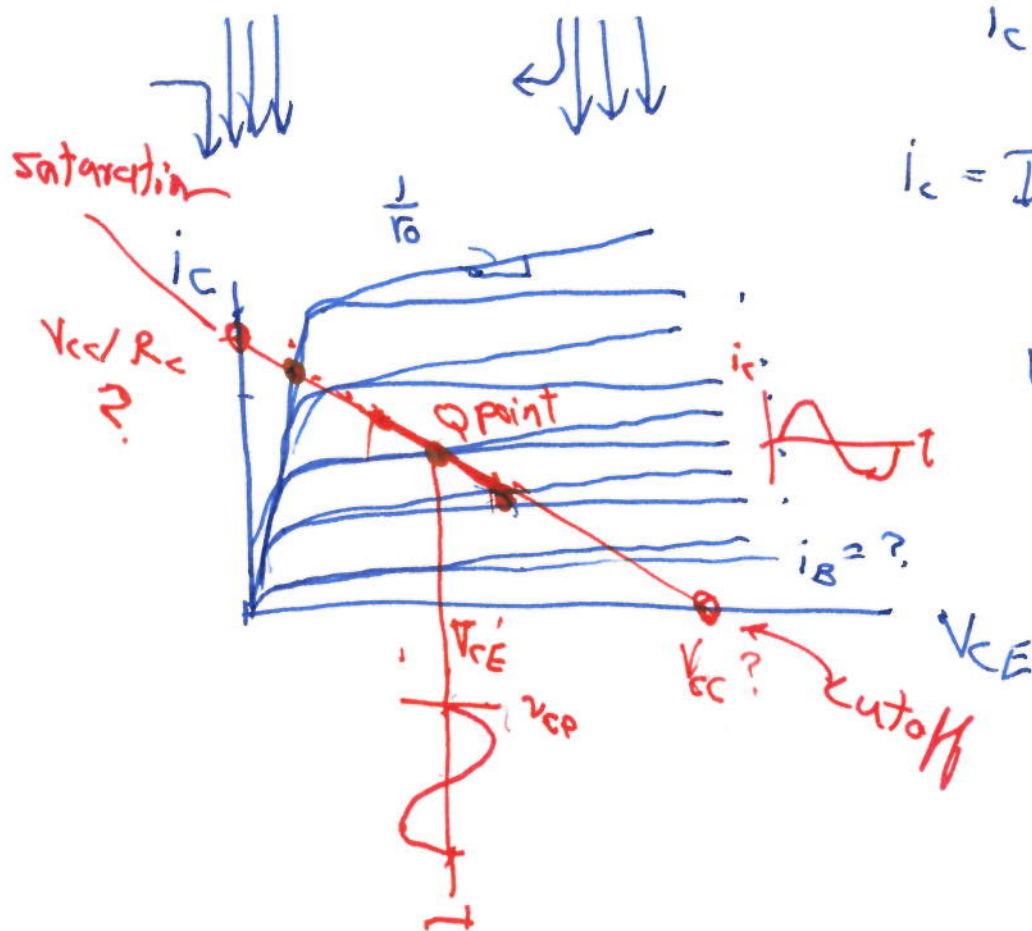
$$i_c = \beta i_B$$

$$i_E = i_C + i_B$$

$$i_E = (B+1) i_B$$

$$i_c = \alpha i_E \quad \alpha = \frac{\beta}{\beta + 1}$$

$$I_c = I_s e^{V_{BE}/V_T}$$



$$r_o = \frac{V_A}{I_C}$$

← Early Voltage  
← DC Current "Q Point"

## Active

$$V_{BE} = 0.7V$$

$$I_c = \beta I_B$$

$V_{CE} > 0.2V$

## Saturation

$$V_{RE} = 0.7 \text{ V}$$

$$V_{CE} = 0.2 \text{ V}$$



cutoff

$$I_C = 0$$

$$V_{CE} = V_{CC}$$

Small signal model

$$V_T = 25 \text{ mV}$$

$$(a) i_c = \beta i_b$$

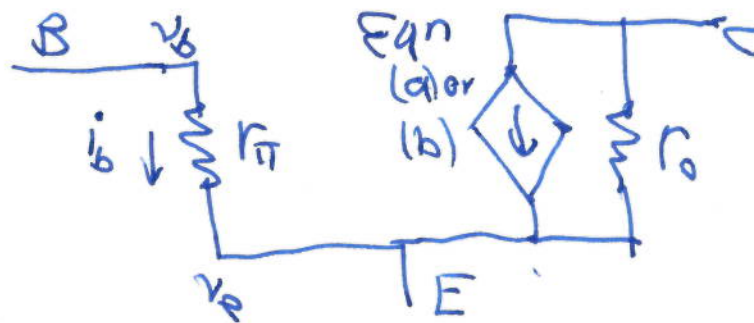
or

$$g_m = \frac{I_C}{V_T}$$

$$(b) i_c = g_m V_{be}$$

$$r_{\pi} = \frac{\beta V_T}{I_C}$$

$$r_o = \frac{V_A}{I_C} \quad \text{or} \quad \left( \frac{d i_c}{d V_{CE}} \right)^{-1}$$

AC circuitSmall  $V_{in}$  or  $i_{in} \rightarrow V_{out}$  or  $i_{out}$  $R_{in}$  $R_{out}$ 

$$A_v = \frac{V_{out}}{V_{in}}$$

$$A_i = \frac{i_{out}}{i_{in}}$$

$$R_{in} = \frac{V_{in}}{i_{in}}$$

$$R_{out} = \frac{V_{out}(R_L \rightarrow \infty)}{i_{out}(R_L \rightarrow 0)}$$

# Typical circuits

Common Emitter

$$|A_v| > 1 \quad |A_i| > 1$$

Common Base

$$A_i \approx 1$$

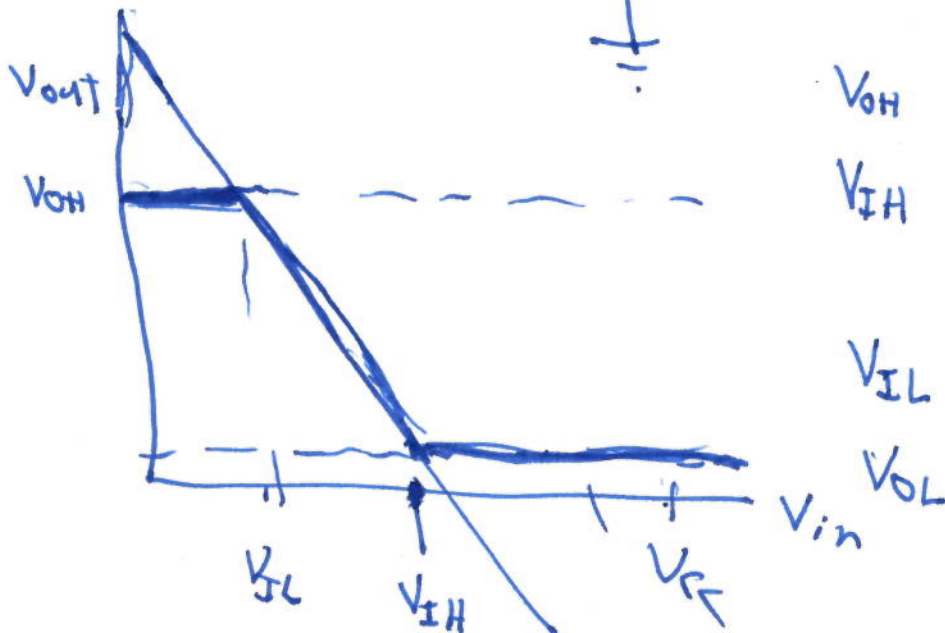
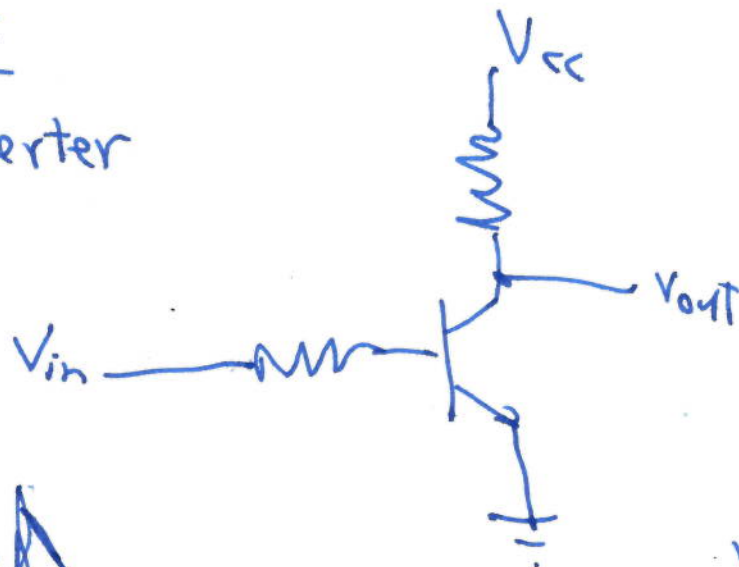
Common Collector

$$A_v \approx 1$$

## 4-Resistor Model.

Logic

Inverter



$V_{OH}$

~~$V_{OH}$~~

$V_{IH}$

~~$V_{IH}$~~

$V_{IL}$

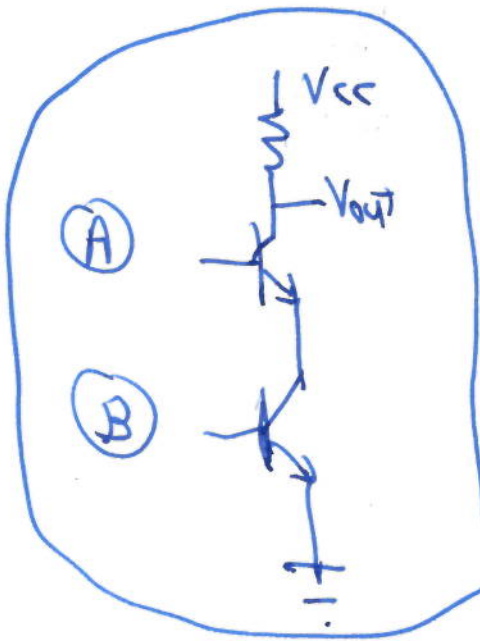
$V_{OL}$

$V_{in}$

$V_{IL}$

$V_{IH}$

$V_{TC}$



| IN |   |  |  |
|----|---|--|--|
| A  | B |  |  |
| L  | L |  |  |
| L  | H |  |  |
| H  | L |  |  |
| H  | H |  |  |

Out

|   |   |
|---|---|
| H | H |
| H | L |
| H | L |
| L | L |

NAND NOR

