Profiling and Debugging

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| Counter | Description |
|-----------------|---|
| GlobalWorkSize | The global work-item size of the kernel. |
| GroupWorkSize | The work-group size of the kernel. |
| KernelTime | Time spent executing the kernel in milliseconds (does not include the kernel setup time). |
| LocalMem | The amount of local memory in bytes being used by the kernel. |
| MemTransferSize | The memory transfer size in bytes. |
| ALU | The ALU instructions executed per thread. |
| Fetch | The fetch instructions executed per thread. |
| Write | The write instructions executed per thread. |
| Wavefront | Total number of wavefronts. |
| ALUBusy | The percentage of time ALU instructions are processed relative to GPU Time. |
| ALUFetchRatio | The ratio of ALU to Fetch instructions. |

| Counter | Description |
|------------------|--|
| ALUPacking | The ALU vector packing efficiency (in percentage). This value indicates how well the Shader Compiler packs the scalar or vector ALU in your kernel to the five-way VLIW instructions. As a rule of thumb, values below 70 percent indicate that ALU dependency chains may be preventing full use of the processor. |
| FlowControlUtil | The kernel flow control instruction use (in percentage). As a rule of thumb, values below 70 percent suggest you could see performance improvements by reducing the number of instructions inside the flow control. |
| FetchUnitBusy | The percentage of time the Fetch unit is active relative to GPU Time. All extra fetches and any cache or memory effects are taken into account. |
| FetchUnitStalled | The percentage of time the Fetch unit is stalled relative to GPU Time. To reduce this percentage, try reducing the number of fetches or reducing the amount per fetch. |
| WriteUnitStalled | The percentage of time the Write unit is stalled relative to GPU Time. |
| Spe | cific to ATI Radeon™ HD 5000 Series Graphics Cards |
| ALUStalledByLDS | The percentage of GPU Time ALU units are stalled because the LDS input queue is full and the output queue is not ready. To reduce this percentage, try to minimize the number of LDS bank conflicts, or reduce the total number of LDS accesses. |
| LDSBankConflict | The percentage of GPU Time the LDS is stalled by bank conflicts. |

- FetchMem data read from global memory
- FastPath and CompletePath data written to global memory (two different buses)
 - FastPath is an optimized hardware path, but supports only simple operations (32-bit+ data types, no atomics)
 - 100+ GB/s measured throughput
 - CompletePath handles sub-32-bit data and advanced operations
 - 20 GB/s measured throughput
 - This bus design is specific to AMD

- L1CacheHit percentage of fetches that hit in L1 memory
 - Texture memory is cached on AMD systems

- Running Example: Matrix Addition
- Data set information
 - 2048 x 2048 threads created
 - 64 threads/wavefront
 - 65536 wavefronts

| Method | GlobalWorkSize | | | rou | pWo | rkSize | Wavefront |
|--------------------|----------------|----|---|-----|-----|--------|-----------|
| matrixadd_086D5C08 | { 2048 2048 | 1} | { | 1 | 64 | 1} | 65536 |
| matrixadd_086D5C08 | { 2048 2048 | 1} | { | 64 | 1 | 1} | 65536 |
| matrixadd_086D5C08 | { 2048 2048 | 1} | { | 8 | 8 | 1} | 65536 |

Fetch Mem (data read from global memory)

- For AMD, memory transactions are based on quarterwavefronts (currently)
 - 64-byte reads
- The workgroup dimensions affect the amount of useful data read each access
 - Reading down a column creates bank conflicts
 - For worst case 4 useful bytes per read (or 1/16th of memory transaction)
 - For 8x8 case, ½ of data is useful

| Method | G | irou | pWo | orkSize | FetchMem |
|--------------------|---|------|-----|---------|----------|
| matrixadd_086D5C08 | { | 1 | 64 | 1} | 524288 |
| matrixadd_086D5C08 | { | 64 | 1 | 1} | 32768 |
| matrixadd_086D5C08 | { | 8 | 8 | 1} | 65536 |

• Memory writes

- No as much information is available about bus design
- No extra penalty for writing 8x8 workgroup
- Just over 4X penalty in bytes written for worst-case scenario

| Method | GroupWorkSize | | FastPath | CompletePath | |
|--------------------|---------------|----|----------|--------------|---|
| matrixadd_086D5C08 | { 1 | 64 | 1} | 71822.88 | 0 |
| matrixadd_086D5C08 | { 64 | 1 | 1} | 16383 | 0 |
| matrixadd_086D5C08 | { 8 | 8 | 1} | 16383.50 | 0 |

- Memory access pattern has a huge effect on performance
 - Biggest performance bottleneck if done incorrectly
- Memory reads from quarter-wavefronts (16 threads) can be coalesced
 - Single memory address sent, multiple data items are retrieved

| Method | GroupWorkSize | FetchMem | FastPath | CompletePath | Time | |
|--------------------|---------------|----------|----------|--------------|----------|--|
| matrixadd_086D5C08 | { 1 64 1} | 524288 | 71822.88 | 0 | 73.89982 | |
| matrixadd_086D5C08 | { 64 1 1} | 32768 | 16383 | 0 | 0.42722 | |
| matrixadd_086D5C08 | { 8 8 1} | 65536 | 16383.50 | 0 | 1.50243 | |
| | | 4 | | | | |

176X faster

• SIMD utilization (i.e., useful work)

- Amount of time ALUs are busy (ALUBusy) * how full they are (ALUPacking)
- Low utilization implies memory latency or not enough threads scheduled

| ALUBusy | ALUPacking |
|---------|------------|
| 0.19 | 35.56 |
| 32.45 | 35.56 |
| 9.34 | 35.56 |

Memory Bandwidth

- Effective memory BW = (Br + Bw)/T
- Br = total number of bytes read
- Bw = total number of bytes written
- T = time required to run the kernel

| WG Dims | 1x64 | 8x8 | 64x1 |
|-------------------|---------|----------|-----------|
| Br | 512MB | 64MB | 32MB |
| Bw | 70.1MB | 16MB | 16MB |
| Т | 73.9ms | 1.5ms | 0.42ms |
| BW _{eff} | 7.7GB/s | 52.1GB/s | 111.6GB/s |

- Dynamic profiler (GPU and CPU)
- Visual Studio plug-in
- Reports performance counters from devices and static information from the binary
 - Works in a limited scope for the CPU (mostly just static information)

• Selecting performance counters

| zionistany · · · · · · · · · · · · · · · · · · · | |
|---|---|
| Select Counters | × |
| Available Counters | |
| Available Counters Image: Count | |
| 18 Selected Counters | |
| | |
| Load Selection Save Selection OK Cancel Help | |
| | |

• Performance counters

| Method | ExecutionOrder | GlobalWorkSize | GroupWorkSize | Time | LocalMemSize | DataTransferSize | GPRs | ScratchRegs | FCStacks | Wavefronts | ALUInsts ^ |
|------------------------|----------------|----------------|---------------|---------|--------------|------------------|------|-------------|----------|------------|------------|
| CreateBuffer | 7 | | | NA | | 0.06 | | | | | |
| CreateBuffer | 8 | | | NA | | 0.06 | | | | | |
| WriteBuffer | 9 | | | 0.56145 | | 1200 | | | | | |
| scan4_k1_Cayman1 | 10 | { 64 480 1} | { 64 1 1} | 0.10590 | 3072 | | 8 | 0 | 3 | 480 | 308 |
| transpose_k2_Cayman1 | 11 | { 640 480 1} | { 16 16 1} | 0.09592 | 1024 | | 3 | 0 | 2 | 4800 | 31 |
| scan4_k1_Cayman1 | 12 | { 64 640 1} | { 64 1 1} | 0.05669 | 3072 | | 8 | 0 | 3 | 640 | 213 |
| transpose_k2_Cayman1 | 13 | { 480 640 1} | { 16 16 1} | 0.03982 | 1024 | | 3 | 0 | 2 | 4800 | 31 |
| CopyBufferToImage2D | 14 | | | 0.21064 | | 1200 | | | | | |
| hessian_det_k3_Cayman1 | 15 | { 640 480 1} | { 16 16 1} | 1.22999 | 0 | | 10 | 0 | 2 | 4800 | 63.24 |
| hessian_detk3_Cayman1 | 16 | { 640 480 1} | { 16 16 1} | 0.09283 | 0 | | 10 | 0 | 2 | 4800 | 63.24 |
| hessian_det_k3_Cayman1 | 17 | { 640 480 1} | { 16 16 1} | 0.09391 | 0 | | 10 | 0 | 2 | 4800 | 63.24 |
| hessian_det_k3_Cayman1 | 18 | { 640 480 1} | { 16 16 1} | 0.09867 | 0 | | 10 | 0 | 2 | 4800 | 63.24 |
| hessian_det_k3_Cayman1 | 19 | { 640 480 1} | { 16 16 1} | 0.10203 | 0 | | 10 | 0 | 2 | 4800 | 48.89 |
| hessian_det_k3_Cayman1 | 20 | { 640 480 1} | { 16 16 1} | 0.10095 | 0 | | 10 | 0 | 2 | 4800 | 48.89 |
| hessian_det_k3_Cayman1 | 21 | { 640 480 1} | { 16 16 1} | 0.10777 | 0 | | 10 | 0 | 2 | 4800 | 48.89 |
| hessian_det_k3_Cayman1 | 22 | { 640 480 1} | { 16 16 1} | 0.10993 | 0 | | 10 | 0 | 2 | 4800 | 48.89 |
| hessian_det_k3_Cayman1 | 23 | { 640 480 1} | { 16 16 1} | 0.07941 | 0 | | 10 | 0 | 2 | 4800 | 42.24 |
| hessian_det_k3_Cayman1 | 24 | { 640 480 1} | { 16 16 1} | 0.06902 | 0 | | 10 | 0 | 2 | 4800 | 42.24 |
| hessian_det_k3_Cayman1 | 25 | { 640 480 1} | { 16 16 1} | 0.08277 | 0 | | 10 | 0 | 2 | 4800 | 42.24 |
| hessian_det_k3_Cayman1 | 26 | { 640 480 1} | { 16 16 1} | 0.07151 | 0 | | 10 | 0 | 2 | 4800 | 42.24 👻 |
| | III | | | | | | | | | | 4 |

• Program trace

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|---------|--|---|------------------------------|-----------------------------|----------------------|--------------------|------------|-------------------------|------|
| | Milliseconds | 1498.802 1536.567 1574.332 1 | 612.097 1649.863 | 1687.628 1725.393 | 1763.158 | 1800.924 | 1838.689 1 | 1876.454 1914.219 | 1951 |
| | ⊟ Host | | | | | | | | |
| | Host Thread 2892 | clBuildProgram | | clEnqueueReadBuff | er | | cl | IEnqueueWriteBuffer | |
| | ⊡ OpenCL | | | | | | | | |
| | Context 0 (0x029426A0) | | | | | | | | |
| | Queue 0 - CPU_Device (0x02942 | 26F0) | | | | | | | |
| | Data Transfer | | | | | | | | |
| : [| Kernel Execution | | scan tran scan | tran hes hes hes hes hhh | h hessian hessian he | essian hessian gi | e g | surf64Descriptor_kernel | |
| : 1 | | | | | | | | | |
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| • | · | | | | | | | | |
| ost Thr | read 2892 Summary | | | | | | | | |
| index | Interface | Parameters | | | | | Result | Device Block | |
| 13 | clEnqueueNDRangeKernel | 0x029426F0; 0x0298DEC0; 3; NULL; [640,480,1]; [16 | ,16,1]; 0; NULL; [0x0BC6DDC8 | 3] | | | CL_SUCCESS | hessian det | |
| .4 | clSetKernelArg | 0x0298DEC0; 8; 4; [0x00000002] | | | | | CL_SUCCESS | | |
| 5 | clSetKernelArg | 0x0298DEC0; 9; 4; [0] | | | | | CL_SUCCESS | | |
| 6 | clEnqueueNDRangeKernel | 0x029426F0; 0x0298DEC0; 3; NULL; [640,480,1]; [16 | ,16,1]; 0; NULL; [0x0BBD1AB8 |] | | | CL_SUCCESS | hessian det | |
| 7 | clSetKernelArg | 0x0298DEC0; 8; 4; [0x00000002] | | | | | CL_SUCCESS | | |
| 8 | clSetKernelArg | 0x0298DEC0; 9; 4; [0x00000001] | | | | | CL_SUCCESS | | |
| 9 | clEnqueueNDRangeKernel | 0x029426F0; 0x0298DEC0; 3; NULL; [640,480,1]; [16 | ,16,1]; 0; NULL; [0x0BBD1BD0 |] | | | CL_SUCCESS | hessian det | |
| 0 | clSetKernelArg | 0x0298DEC0; 8; 4; [0x00000002] | | | | | CL_SUCCESS | | |
| | | | | | | | CL_SUCCESS | | |
| L | clSetKernelArg | 0x0298DEC0; 9; 4; [0x00000002] | | | | | | | |
| 1 2 | clSetKernelArg clEngueueNDRangeKernel | | ,16,1]; 0; NULL; [0x0BBD1CE8 |] | | | CL_SUCCESS | hessian det | |
| | | 0x02980EC0; 9; 4; [0x00000002] 0x029426F0; 0x0298DEC0; 3; NULL; [640,480,1]; [16 | ,16,1]; 0; NULL; [0x0BBD1CE8 |] | | | CL_SUCCESS | <u>hessian det</u> | |

NVIDIA Visual Profiler

• Similar to AMD Stream Profiler

- View timing information
- Presents hardware utilization statistics (occupancy)
 - Register usage, % of max threads, etc
- Provides performance counters

• PROTIP: If you don't release all of your OpenCL resources, the profiler throws a cryptic error message

NVIDIA Visual Profiler

| | | CUDAF - C | JDA Visua | l Profiler | - [Test1 - D | evice 0 - C | ontext_0] (o | n danger3) | × | | | |
|------------|---|-----------------|-----------|------------|--------------|-------------|--------------|--------------------------------|--|--|--|--|
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| Profile | Profiler Output × | | | | | | | | | | | |
| | GPU Timestamp 🔺 | Method | GPU Time | CPU Time | Occupancy | grid size X | block size X | static shared memory per block | registers per thre $\begin{bmatrix} \bullet \\ \vdots \end{bmatrix}$ | | | |
| 1 | 0 | memcpyHtoD | 7002.34 | 7186 | | | | | | | | |
| 2 | 13694.7 | memcpyHtoD | 6994.08 | 7177 | | | | | | | | |
| 3 | 20790.3 | compute1_kernel | 2069.57 | 2090 | 1 | 39063 | 256 | 44 | 9 | | | |
| 4 | 22909.7 | memcpyDtoH | 7387.52 | 7613 | | | | | | | | |
| 5 | 45311.2 | memcpyHtoD | 6994.46 | 7177 | | | | | | | | |
| 6 | 58975.2 | memcpyHtoD | 6987.26 | 7171 | | | | | | | | |
| 7 | 66030.6 | compute1_kernel | 2068.96 | 2082 | 1 | 39063 | 256 | 44 | 9 | | | |
| 8 | 68146.2 | memcpyDtoH | 7391.36 | 7612 | | | \$ | | | | | |
| 9 | 90497 | memcpyHtoD | 6998.08 | 7183 | | | | | | | | |
| 10 | 104164 | memcpyHtoD | 7010.66 | 7194 | | | | | | | | |
| 11 | 111241 | compute1_kernel | 2068.38 | 2082 | 1 | 39063 | 256 | 44 | 9 | | | |
| 12 | 113357 | memcpyDtoH | 7396.1 | 7620 | | | | | | | | |
| 13 | 135712 | memcpyHtoD | 7209.31 | 7391 | | | | | | | | |
| 14 | 149602 | memcpyHtoD | 6995.46 | 7181 | | | | | | | | |
| • | | • • • • | | | - | | | · · · | • • | | | |

NVIDIA Parallel NSIGHT Debugger

• Parallel NSIGHT Debugger

- Live GPU Debugger
 - Not available for OpenCL
- Can set breakpoints in GPU code, single step through code
- Can view memory contents (global, shared, local memory)

NVIDIA Parallel NSIGHT Debugger

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| | | | d memory array 3 | 10 for | coordinates 10, 0 | | Br Warp 0 (0.0.0) | Active Mask: 0xFFFFFFFF, PC: 0x000024B0, matrixmul_kernel.cu.91 | | | | | | |
| | | sub-matrix of 3 | | | for coordinates 10, 5 | | Br Warp 1 (0.2.0) | Active Mask: 0xFFFFFFFF, PC: 0x00002480. matrixmul_kernel.ou:91 | | | | | | |
| | _spared_ II | oue waterors 21 | IZE] (BLOCK_SIZE) | | - | | Ib: Warp 2 (0.4.0) | Active Mask: 0xFFFFFFF, PC: 0x000024B0, matrixmul_kernel.cu:91 | | | | | | |
| × | | | m | | OK. | Cancel | E Warp 3 (0,6,0) E Warp 4 (0,6,0) | Active Mask: 0xFFFFFFF, PC: 0x00002480, matrixmul_kernel.cu/91 &-tive Mask: 0xFFFFFFF, PC: 0x00002480, matrixmul_kernel.cu/91 | | | | | | |
| Memory 1 | | | | | | × | | | - 3 × | | | | | |
| Address 0x00 | 1113:00 | | | | + (/) Columns: Auto | | Name | Value | Type + | | | | | |
| 0w00113048 | 0.17999817 | 0.04229562 | 0.18735313 | 0.81136816 | qQ8>_A">=07>.4.7 | | B) I blockida | $\{x = 4, y = 0, z = 0\}$ | const uni | | | | | |
| 0w00113C5# | 0.89681691 | 0.36292613 | 0.61283306 | | £.#758.>.I.7X8+7 | | (8) 🔹 blockDim (8) 🥥 gridDim | $\{x = 16, y = 16, z = 1\}$ $\{x = 8, y = 5, z = 1\}$ | const dm | | | | | |
| 0x00113C68 0x00113C78 | 0.10892056 0.71721540 | 0.33661917 0.30536821 | 0.69341105 | | | | a a | $(x = a_1 y = 3, z = 4)$ 222 | int int | | | | | |
| 0x00113088 | 0.27188939 | 0.93353069 | 0.40219121 | 0.33423871 | .5.>0dn741.7V1e> | | a p | 200 | int | | | | | |
| 0x00113C98 0x00113C88 | 0.32767725 | 0.067171238 0.56959748 | 0.32349619 0.96176022 | | 24728.1.#E;¥>59:7 | | 🥥 bu | 4 | nt | | | | | |
| 0x00113CEF | 0.32789087 | 0.46266061 | 0.59346294 | | Pág>Úái>01.7:e.7 | | 🤣 by | 0 | int | | | | | |
| 0x00113008 | 0.75220125 | 0.90661935 | 0.72939131 | | | | e bi | 14 | int int | | | | | |
| 0x00113CD8 0x00113CE8 | 0.11154515 0.56514174 | 0.048829618 0.27295756 | 0.79274881 0.54741049 | | žqk=H=-5J7esU7 17.X.>.#.71.>7 | | allegin | 9 | int | | | | | |
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| 0x00113000 | 0.76705223 | 0.73012403 | 0.44430002 | | .107v6:70.8x41'7 | 1.0 | aStep | 16 | int a | | | | | |
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| 0x00113030 | 0.60927153 | 0.87200534 | 0.43308915 | 0.40858179 | #4.7.;_7#.¥>#18> | | bStep Csub | 2048 | float | | | | | |
| 0x00113D40 | 0.00590207 | 0.60106206 | 0.43745232 | | ETB745.7Å03>µII> | | e cuo | 177 | int | | | | | |
| 0x00113068 | 0.04151735 0.91659290 | 0.15988646 0.15057832 | 0.00518935 0.69682914 | | "mR7G.#>Å.≥7/µ-> OWj741.>=c27.1\> | | a oc | 0x00119c00 0 | _device | | | | | |
| De00113D78 | 0.99294014 | 0.82775354 | 0.99901609 | 0.41434979 | +;17 987.5.7 40> | | 91 🧳 A | 0x00110000 0.20108646 | device_ | | | | | |
| 0x00110D08 0x00110D00 | 0.72759181 0.20931546 | 0.57005525 | 0.00012902 | | uC:7\$1.7+.V7g.87 | | H 🕹 🖲 | 0x00113c00 0.80645362 | device | | | | | |
| De00113080 | 0.30326244 | | | | | | Ax Q De Q | 48 | shared | | | | | |
| 0x00113088 | 0.0097964415 | 0.33207190 | 0.8960383 | | A. <7.*>28e72.4; | - | | 100 | | | | | | |
| | A. 55406/661 | | | 5. ANT. J. CO. | and the rest of the | | | | | | | | | |

NVIDIA Parallel NSIGHT Analyzer

- Visualization of program execution flow
 - Kernel calls
 - Driver functions calls
 - Memory transfers
- Similar to AMD Stream Profiler

NVIDIA Parallel NSIGHT Analyzer

| Fie E | de | View Proje | a B | uid Deb | ug Nsight Too | its Test V | indow He | slp | | | | | | | | | | | | | | | | |
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| E Pr | oces | | | | | | 1.1.1 | | | | | | | | | | | | | | | | | |
| 8 | [114 | 40] | | | | | | | | | | | | | | | | | | | | | | |
| 8 | | ead (6816) | | | | | | | | | | | | | | | | | | | | | | |
| | CUE | unction Calls | ×. | | cuEvent cuEvents | - cuMemo | pyDtoH | cuMemop | yDtoH | ouMemop | yDtoH | cuMettrayD | 1071 | cuMemopy(| NoH c | uMemcpyD | toH cuMe | ncpyOtoH | cuMemb | lyDtoH | cullemopy | DtoH ov | Memcpy010 | |
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| | | | V. | | | | | | | | | | | | | | | | | | | | | |
| 8 | Ce | ontext 1 (0) | | | | | | | | | | | | | | | | | | | | | | |
| | | Driver API | Χ. | | cuEvent cuEventS. | outviento | and the state of the | cuMemos | | ouMemop | A REAL PROPERTY AND ADDRESS OF | cuMemcpyD | | cuMemcpy(| | uMemcpyO | | ncpyOtoH | cuMemos | | cuMemcpy | | MemcpyOto | |
| | | Memory | 1 | | 65536K | - | 65536K. | - | 65536K_ | Concession of the local division of the loca | 65536K | Contract Con | ster. | | 55368 | | \$36K | ESCIENT. | | 65536K | | 55.56K | Baladara (| 36K. |
| | 8.4 | 47.3% [51 | 10 | | | r intjarray intjarray | | In/Carray | | int, amay | | init_array init_array | | nst_array nst_array | | Larray | internation | | init_array | | init_array | | array | |
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| | 8.4 | Stream 4 Counters | - | | | | | | | | | | | | | | | | | | | | | |
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