

Super-Convincing Thesis/Dissertation Title

A Dissertation Presented

by

Hubert Husky

to

The Department of Electrical and Computer Engineering

in partial fulfillment of the requirements

for the degree of

Doctor of Philosophy

in

Electrical and Computer Engineering

Northeastern University

Boston, Massachusetts

December 2014

To my family.

Contents

List of Figures

List of Tables

List of Acronyms

AHB Advanced High-performance Bus. System bus definition within the AMBA 2.0 specification. Defines a high-performance bus including pipelined access, bursts, split and retry operations.

ATLM Arbitrated Transaction Level Model. A model of a system in which communication is described as transactions, abstract of pins and wires. In addition to what is provided by the **TLM**, it models arbitration on a bus transaction level.

TLM Transaction Level Model. A model of a system in which communication is described as transactions, abstract of pins and wires.

Acknowledgments

Here I wish to thank those who have supported me during the process of the thesis work...

Abstract of the Dissertation

Super-Convincing Thesis/Dissertation Title

by

Hubert Husky

Doctor of Philosophy in Electrical and Computer Engineering

Northeastern University, December 2014

Dr. Advisor, Advisor

This is a very abstract abstract.

Chapter 1

Introduction

This is where the intro goes. There is a planet ... where people still believe digital watches are the greatest invention. It is mostly harmless.

1.1 Life, Universe and Everything

Simple, quantitative answers to the most pondering questions are beautiful. More about question of life, universe and everything can be found in [?].

This is where the text goes. One can refer to a previous chapter like Chapter ??. To find all the other reference possibilities search in the MACROS folder for chapref. Also one can include pictures, preferably in the pdf format like shown in Figure ??. Graphics can also be included as eps, if graphics is in eps, the epstopdf packages converts it to pdf on the fly. See examples below in Figure ??.

To keep the code clean, call all extra packages that you would like to use from the file macro.tex in the macro folder. Also keep all your graphics in the fig folder and all bibliography related files in the bib folder.

Make sure that you run bibtex and makeindex to generate bibliography and index.

This demonstrates how to include a term into the index. For details, check out [?].

Also tables are possible as shown in Table ??. What is really cool are the acronyms. The first time you use one, like **TLM!** (**TLM!**) then it appears with full description. Using it the second time makes only the acronym appears **TLM!**.

CHAPTER 1. INTRODUCTION

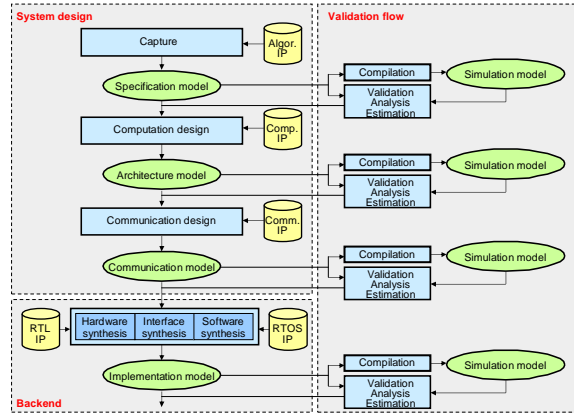


Figure 1.1: Design methodology for SoC design (Source [?])

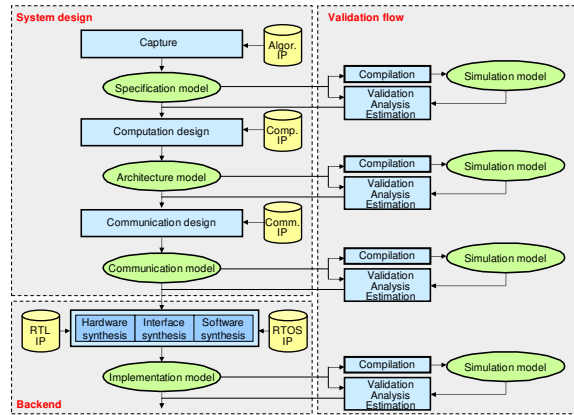


Figure 1.2: Design methodology but included as EPS figure

Environment Condition	Applicable Model
<ul style="list-style-type: none"> • single master bus • no overlap between masters bus access 	TLM!
<ul style="list-style-type: none"> • only locked transfers • unlocked transfers and low overlap 	ATLM! (ATLM!)
<ul style="list-style-type: none"> • unlocked transfers and high overlap 	bus functional

Table 1.1: Conclusion summary

CHAPTER 1. INTRODUCTION

Now time for some math:

$$F(x) = \int_{-\infty}^x f(u) du \tag{1.1}$$

$$\bm{Ax} = \bm{b} \tag{1.2}$$

$$\mathbf{Ax} = \mathbf{b} \tag{1.3}$$

$$\sin^2(\theta) + \cos^2(\theta) = 1 \tag{1.4}$$

$$x \in \mathcal{X} \tag{1.5}$$

The difference in using `\bm` and `\mathbf` is clear from above.

Chapter 2

Conclusion

Writing a long manuscript is easy ... only if one starts early enough.

Appendix A

First Appendix Headline

Appendix B

Second Appendix Headline